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SERIAL NUMBER 10/629,337	FILING DATE 07/28/2003  RULE	CLASS 257	GROUP ART UNIT 2822	ATTORNEY DOCKET NO. ACT-368
APPLICANTS  John McCollum, Saratoga, CA;  ** CONTINUING DATA ***** None ** FOREIGN APPLICATIONS ***** None				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 10/23/2003				
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Verified and Acknowledged Examiner's Signature Initials		STATE OR COUNTRY CA	SHEETS DRAWING 2	TOTAL CLAIMS 36  INDEPENDENT CLAIMS 8
ADDRESS 28661 SIERRA PATENT GROUP, LTD. P O BOX 6149 STATELINE , NV 89449				
TITLE Transistor having fully-depleted junctions to reduce capacitance and increase radiation immunity in an integrated circuit				
FILING FEE  RECEIVED 1618	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____	

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